

## ADDRESS COMPLEXITY OF SYSTEM ON CHIP (SOC) DESIGN



### CHALLENGE

- A context where the increasing size and complexity of system on chip (SoC) design combines with a shorter time to market.
- Manage the lifecycle of complex systems and reduce their design cost.
- Facilitate information exchange and optimize CAD design flows.

### BENEFITS

- Maximize design and IP reuse
- Control design flows
- Optimize design flow architecture [\[More...\]](#)


### SOLUTIONS

Magillem assembly and connectivity software suite, based on the worldwide IP-XACT exchange standard (IEEE 1685), enables you to :

- Package all components of an IP suite into XML, in compliance with the IP-XACT schema [\[More...\]](#)

### RELATED RESOURCES

- Flow architecture: Magillem Flow Architect (MFA) [\(please, contact us\)](#)
- Connectivity: Magillem Platform Assembly [\[More...\]](#)

 [Contact us for more information](#)

